What is claimed is:

1. A load coil, comprising:

a coupled inductor having a first winding having a first intra-winding capacitance and a second winding having a second intra-winding capacitance, the first and second windings wound about an inductor core, the first and second windings having an inter-winding capacitance;

a first capacitor disposed in parallel with the first winding and a second capacitor disposed in parallel with the second winding for increasing the impedance of the load coil to certain signals above 4 kHz.

2. The load coil according to claim 1, wherein a ratio of the inter-winding capacitance to the combined capacitance of the first intra-winding capacitance and the first capacitor is in the range of 0.75 – 1.25.

3. The load coil according to claim 1, wherein a ratio of the inter-winding capacitance to the combined capacitance of the first intra-winding capacitance and the first capacitor is in the range of 0.99 – 1.01.

4. The load coil according to claim 1, wherein the first and second capacitors each have a capacitance in the range of 770 – 1290 pF.

5. The load coil according to claim 1, wherein the inter-winding capacitance is in the range of 1,030 – 1,050 pF and the capacitance of each of the first and second capacitors is in the range of 770 – 1290 pF.

6. The load coil according to claim 1, wherein the certain signals above 4 kHz further comprise signals in the range of 25 kHz – 1.1 MHz.

7. A load coil, comprising:

a coupled inductor having an inter-winding capacitance, an intra-winding capacitance, and an inductance;

the ratio of the inter-winding capacitance to the intra-winding capacitance being in the range of about 0.75 – 1.25 for increasing the impedance of the load coil to signals in the range of 25 kHz – 1.1 MHz.

8. The load coil according to claim 7, wherein the ratio of the inter-winding capacitance to the intra-winding capacitance is in the range of about 0.99 to 1.01

9. A load coil, comprising:

a first inductor including a first winding and a first core, the first winding having upstream and downstream ends and a first intra-winding capacitance;

a second inductor including a second winding and a second core, the second winding having upstream and downstream ends and having a second intra-winding capacitance;

a first capacitor disposed between the upstream end of the first inductor and the downstream end of the second inductor to offset at least a portion of the first and second intra-winding capacitances for improving the impedance of the load coil to DSL-band signals; and

a second capacitor disposed between the upstream end of the second inductor and the downstream end of the first inductor to offset at least a portion of the first and second intra-winding capacitances for improving the impedance of the load coil to DSL-band signals.

10. A load coil for being coupled to a local loop to improve transmission of POTS band signals over the local loop, comprising:

a first inductor having a first winding and a first core, the first inductor for improving transmission of POTS band signals on a first wire of the local loop; and

a second inductor having a second winding and a second core for improving transmission of POTS band signals on a second wire of the local loop.

11. A DSL repeater for improving transmission of POTS band and DSL band signals over a local loop, the repeater comprising:

an upstream signal amplifier for amplifying upstream DSL signals;

a downstream signal amplifier for amplifying downstream DSL signals; and

a load coil disposed in parallel with the upstream and downstream signal amplifiers for improving the transmission of POTS band signals over the local loop.

12. The DSL repeater according to claim 11, wherein the load coil further comprises:

a coupled inductor having a first winding having a first intra-winding capacitance and a second winding having a second intra-winding capacitance, the first and second windings wound about an inductor core, the first and second windings having an inter-winding capacitance;

a first capacitor disposed in parallel with the first winding and a second capacitor disposed in parallel with the second winding for increasing the impedance of the load coil to the upstream and downstream DSL signals.

13. The DSL repeater according to claim 12, wherein a ratio of the inter-winding capacitance to the combined capacitance of the first intra-winding capacitance and the first capacitor is in the range of 0.75 – 1.25.

14. The DSL repeater according to claim 12, wherein a ratio of the inter-winding capacitance to the combined capacitance of the first intra-winding capacitance and the first capacitor is in the range of 0.99 – 1.01.

15. The DSL repeater according to claim 12, wherein the first and second capacitors each have a capacitance in the range of 770 – 1290 pF.

16. The DSL repeater according to claim 12, wherein the inter-winding capacitance is in the range of 1,030 – 1,050 pF and the capacitance of each of the first and second capacitors is in the range of 770 – 1290 pF.

17. The DSL repeater according to claim 11, wherein the load coil further comprises:

a first inductor having a first winding and a first core, the first inductor for improving transmission of POTS band signals on a first wire of the local loop; and

a second inductor having a second winding and a second core for improving transmission of POTS band signals on a second wire of the local loop.

18. The DSL repeater according to claim 11, wherein the load coil further comprises:

a first inductor including a first winding and a first core, the first winding having upstream and downstream ends and a first intra-winding capacitance;

a second inductor including a second winding and a second core, the second winding having upstream and downstream ends and having a second intra-winding capacitance;

a first capacitor disposed between the upstream end of the first inductor and the downstream end of the second inductor to offset at least a portion of the first and second intra-winding capacitances for improving the impedance of the load coil to DSL-band signals; and

a second capacitor disposed between the upstream end of the second inductor and the downstream end of the first inductor to offset at least a portion of the first and second intra-winding capacitances for improving the impedance of the load coil to DSL-band signals.